



PATENT

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March 19, 2002

Date

Denise Sheridan

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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant	: Leland R. Revill	Attorney Docket No.:	500060.02
Serial No.	: 09/998,594	Group Art Unit	: 2876
Filed	: November 16, 2001	Examiner	: Not yet assigned
Title	: METHOD AND APPARATUS FOR IDENTIFYING INTEGRATED CIRCUITS		

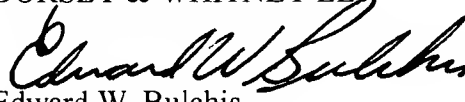
GENERAL AUTHORIZATION UNDER 37 C.F.R. § 1.136(a)(3)

Box Missing Parts
Commissioner of Patents
Washington, D.C. 20231

Sir:

With respect to the above-identified application, the Commissioner is authorized to treat any concurrent or future reply requiring a petition for an extension of time under 37 C.F.R. § 1.136(a)(3) for its timely submission as incorporating a petition therefor for the appropriate length of time. The Commissioner is also authorized to charge any fees which may be required, or credit any overpayment, to Deposit Account No. 50-1266.

Respectfully submitted,
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